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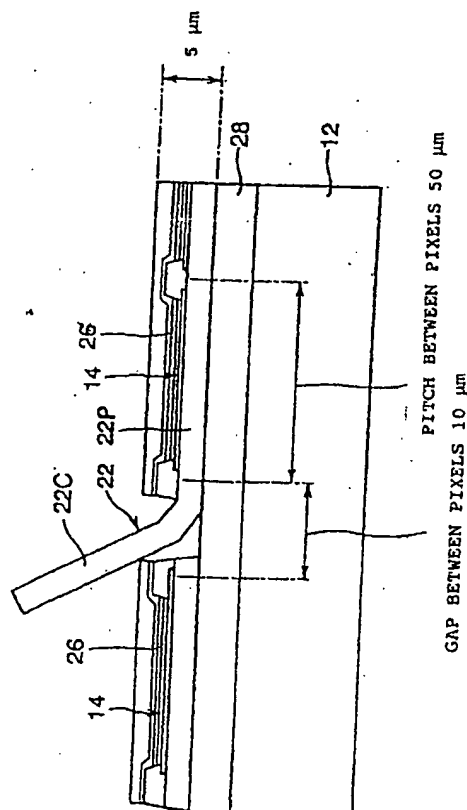
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(54) **LARGE EL PANEL AND PRODUCTION METHOD THEREFOR**

(57) A circuit section 22C of a TFT layer 22 is disposed behind an adjacent EL display device 14, so that the gap between pixels at the peripheries of the adjacent EL display devices 14 may be 10 μm . Thus, four EL display devices 14 appear to be unified, forming the large EL display panel 10. In addition, in the case in which a plurality of EL display devices are arranged in a matrix pattern, the pitch between the pixels provided in the pixel section of the TFT array is maintained constant.

Fig. 8



Description

Technical Field

[0001] The present invention relates to a large EL panel formed by a plurality of EL display devices such as organic EL panels, which are arranged in a matrix pattern, and also relates to a manufacturing method for the large EL panel.

Background Art

[0002] Small EL flat panels (EL display devices) driven by polysilicon TFTs are usually laminated on a hard and transparent substrate by an adhesive, etc., in order to maintain the flatness thereof. The TFTs control the driving of pixels in the EL display devices individually, so that plane images are displayed.

[0003] The EL display devices as described above are typically two to six inches in diagonal, and provide excellent functions as small image displays.

[0004] Conventionally, in theaters and stadiums, large screens which are capable of displaying clear images are commonly used in place of electrical scoreboards. In such large screens, a plurality of light sources, such as the above-described EL display devices, light bulbs are arranged in a matrix pattern. Each of the light sources functions as a pixel for forming an image.

[0005] In such theaters and stadiums, there is a sufficient distance between the screen and the audience; therefore, the EL display devices having a diagonal size of, for example, several inches should not be a problem.

[0006] Recently, on the other hand, instead of CRTs, liquid crystal display panels have been commonly used as displays for televisions and personal computers. In addition, there has been a demand to increase the sizes of the liquid crystal display panels.

[0007] In a liquid crystal display, an additional light source is required as a backlight. Thus, to increase the size of the liquid crystal display, the size of the backlight must also be increased. The present situation, however, is such that when a large (50 to 100 inches in diagonal) and thin display is required, an amount of heat, which increases along with the size of the backlight, will be too large. In addition, the thickness of the display cannot be made thin enough to satisfy the requirement.

[0008] Accordingly, the use of the EL display devices driven by TFTs, which will be referred to as TFT-EL display devices in the following, has been considered. In the TFT-EL display devices, the backlights are not required. In addition, due to the use of the TFTs, the number of pixels may be increased, and response rate becomes higher, so that high-resolution images may be displayed.

[0009] Large TFT-EL display devices, however, have not been realized. Accordingly to form a large display panel which is 20 to 100 inches in diagonal, small TFT-EL display devices which are several inches in diago-

nal, must be arranged in a matrix pattern.

[0010] Generally, a TFT-EL display device is laminated on a flat-type transparent substrate. Accordingly, TFT-EL display devices cannot be arranged in proximity to each other beyond the peripheries of the transparent substrates.

[0011] In addition, a TFT array includes not only a pixel section but also a circuit (driver) section for individually controlling the light emission at the pixels. The circuit section is disposed at a region outside an image forming area, and prevents the adjacent EL display devices from being arranged in close proximity to each other.

[0012] In consideration of the above-described facts, an object of the present invention is to provide a large EL display panel and a manufacturing method therefor, in which a plurality of EL display devices are arranged in a matrix pattern, and in which a pitch between the pixels in the pixel section of the TFT array is maintained constant.

Disclosure of Invention

[0013] The present invention provides a large EL panel comprising a plurality of EL display devices which are arranged in a matrix pattern on a main transparent substrate which is capable of supporting multiple EL display devices, each of the EL display devices including: a base layer over which a luminescent material is applied; an electrode layer which is laminated on one side of the base layer; and a TFT layer including a circuit section in which light emission of the luminescent material is controlled by applying a predetermined voltage between the electrode layer and the TFT layer and a pixel section which is superimposed over the other side of the base layer and which is provided with a plurality of pixels which divide the base layer into sections so that light emission of the luminescent material in each section is individually controlled by generating a potential difference between the electrode layer and the TFT layer at the corresponding section, wherein the circuit section of the TFT layer which is disposed at the region outside a light emitting area is disposed behind the adjacent EL display device so that light emitting areas of the adjacent EL display devices are arranged in proximity to each other with a predetermined amount of gap.

[0014] In addition, the present invention also provides a manufacturing method for a large EL panel in which a plurality of EL display panels are used, each of the EL display panels being constructed of the above-described EL display device and a sub transparent substrate for supporting the EL display device, the manufacturing method for the large EL display panel comprising the steps of: removing the EL display devices from the sub transparent substrates; arranging the obtained EL display devices on a main transparent substrate, which is broader than the sub transparent substrate, in a matrix pattern and in a manner such that light emitting

areas of the adjacent EL display devices are in proximity to each other; and disposing and fixing the circuit section of the TFT layer, which is disposed at the region outside the light emitting area, behind the adjacent EL display device.

[0015] In the case in which a large display panel is formed by using the EL display devices, there has been a problem in that sub transparent substrates, which are larger than the EL display devices, prevent the EL display devices from being arranged in proximity to each other. Accordingly, by applying, for example, separation and transfer techniques described in Japanese Unexamined Patent Application Publication Nos. 10-125930 and 10-125931, it is possible to arrange the EL display devices in proximity to each other.

[0016] With the above-described techniques, the EL display devices may be separated from the sub transparent substrates by applying a mechanical or chemical force to adhesive layers which are disposed therebetween, and may be transferred to another substrate.

[0017] Each of the TFT layers has a pixel section and a circuit section. The pixel section is superimposed over the light emitting area, so that no problems occur. In contrast, however, the circuit section is disposed at the periphery, usually at two sides, of the TFT layer, and at the region outside the light emitting area. Accordingly, the circuit section prevents the adjacent EL display devices from being arranged in proximity to each other, so that the ability of the TFTs to display high-resolution images cannot be sufficiently exploited. According to the present invention, however, the circuit section is disposed behind the adjacent EL display device, so that a high resolution and large EL display panel may be constructed.

[0018] The manufacturing process of the large EL display panel will be described below. First, the EL display devices are removed from the sub transparent substrates by using the above-described separation and transfer technique. Then, the EL display devices are arranged on a main transparent substrate, which is broader than the sub transparent substrate, in a matrix pattern in a manner such that the light emitting areas of the adjacent EL display devices are in proximity to each other. And then, the circuit section of the TFT layer, which is disposed at the region outside the light emitting area, is disposed and fixed behind the adjacent EL display devices.

[0019] According to the present invention, the EL display devices are several inches in diagonal, and the main transparent substrate is 20 to 100 inches in diagonal.

[0020] In addition, the predetermined amount of gap between the light emitting areas of the EL display devices is approximately the same as the pitch between pixels provided in the pixel section of the TFT layer.

[0021] To dispose the circuit section behind the adjacent EL display device, the TFT layer is bent at the boundary between the pixel section and the circuit section.

tion.

[0022] Alternatively, a step portion for changing the positions of the adjacent EL display devices in the thickness direction may be provided so as to dispose the circuit section behind the adjacent EL display device.

[0023] Alternatively, the adjacent EL display devices may be inverted so as to arrange the base layers of the adjacent EL display devices in the same plane.

10 Brief Description of the Drawings

[0024]

Fig. 1 is a front view of a large EL panel according to a first embodiment.

Fig. 2 is an enlarged view of a part of Fig. 1.

Fig. 3 is a sectional view of an EL display device.

Fig. 4 is a sectional view of the EL display device and a sub transparent substrate, which are separated from each other.

Fig. 5 is a sectional view of EL display devices which are adjacently disposed on a main transparent substrate.

Fig. 6 is a perspective view showing a manner in which circuit sections of TFT layers are disposed under the adjacent EL display devices.

Fig. 7 is a front view of the EL display devices which are disposed adjacently to each other on the main substrate.

Fig. 8 is a detailed sectional view of the adjacent EL display devices which are disposed on the main substrate.

Fig. 9 is a sectional view of a large EL panel according to a second embodiment of the present invention, showing a construction in which the circuit section of the TFT layer is disposed.

Fig. 10 is a sectional view of a large EL panel according to a third embodiment of the present invention, showing a construction in which the circuit section of the TFT layer is disposed.

Fig. 11 is a front view of TFT layers including circuit sections, which are incorporated in a fourth embodiment of the present invention.

Fig. 12 is a front view of TFT layers including circuit sections, which are incorporated in a fifth embodiment of the present invention.

Fig. 13 is a process chart of a manufacturing process for EL panels.

50 Best Mode for Carrying Out the Invention

[0025] The present invention will be explained below in conjunction with the accompanying drawings.

55 (First Embodiment)

[0026] Fig. 1 shows a large EL display panel 10 according to the present embodiment. The large EL dis-

play panel 10 includes a main transparent substrate 12 on which four EL display devices 14A, 14B, 14C, and 14D are arranged in a matrix pattern (in the present embodiment, $x \times y = 2 \times 2$). The EL display devices 14A, 14B, 14C, and 14D have the same construction, and are denoted as EL display devices 14 below when they are generically described.

[0027] As shown in Fig. 2, the EL display devices 14, which are driven by polysilicon TFTs, are divided into a plurality of pixels, and light emission (and gradation) at each pixel is controlled individually. The EL display devices 14 are limited in size: a single EL display device is several (2 to 6) inches in diagonal. Thus, to form a display, which is, for example, a dozen inches to a hundred inches in diagonal, a plurality of EL display devices 14 must be arranged in a matrix pattern.

[0028] In Fig. 1, a display which is approximately 20 inches in diagonal is constructed by the four EL display devices 14. This size is approximately the same as the size 'A3' according to the Japanese Industrial Standard (JIS).

[0029] Fig. 3 shows a sectional view of one of the EL display devices 14.

[0030] The EL display device 14 incorporated in the present embodiment is prepared as a product, that is, an EL panel 16, in which the EL display device 14 is laminated on a sub transparent substrate 18 via an adhesive layer 20.

[0031] More specifically, in the EL panel 16, the EL display device 14 is supported by the sub transparent substrate 18 in a manner such that the EL display device 14 is parallel to the sub transparent substrate 18. In the present embodiment, the EL display device 14 is separated from the sub transparent substrate 16 at the adhesive layer 20, as shown in Fig. 4. By using a separation and transfer technique, only the EL display device 14 is removed from the sub transparent substrate 18.

[0032] The EL display device 14 is constructed of a laminate including a plurality of layers.

[0033] The bottom layer of the EL display device 14 is a TFT layer 22 which includes a pixel section 22P and a circuit section 22C. The pixel section 22P is divided in a matrix pattern, and is provided with a plurality of pixels. Light emission of a fluorescent material, which will be described below, at each pixel is controlled individually.

[0034] The circuit section 22C includes a driver for controlling the light emission at each pixel, and is disposed along two adjoining sides of the TFT layer 22.

[0035] A base layer 24 on which the fluorescent material is applied is laminated over the pixel section 22P of the TFT layer 22. In addition, a transparent electrode layer 26 is laminated so as to cover both the base layer 24 and the circuit section 22C. The transparent electrode layer 26 also functions as a protection layer.

[0036] When a current is applied to some particular pixels by the driver included in the circuit section 22C of the TFT layer 22, a potential difference is generated between the TFT layer 22 and the transparent electrode

layer 26. Accordingly, the fluorescent material contained in the base layer 24 at the corresponding positions emits light. In the present embodiment, the pixels are divided into groups of three, each group providing three colors, RGB, so that color images may be displayed.

[0037] Now, the above-described case is considered in which the four EL display devices 14 are arranged and laminated on the main transparent substrate 12 via an adhesive layer 28. In such a case, the circuit section 22C of the TFT layer 22 would cause a large gap between the pixels disposed at the peripheries of two adjacent EL display devices 14. Thus, in the present embodiment, the TFT layer 22 is bent at the boundary between the circuit section 22C and the pixel section 22P so as to dispose the circuit section 22C behind the adjacent EL display device 14, as shown in Figs. 5 to 7. Accordingly, the pixels at the peripheries of adjacent EL display devices 14 may be arranged in proximity to each other.

[0038] As shown in Fig. 8, the gap between the pixels at the peripheries of adjacent EL display devices 14 may be reduced to 10 μm . The pitch between the pixels is 50 μm , the thickness of the TFT layers is 1 μm , and the thickness of the EL display devices 14 is 5 μm .

[0039] The operation of the present embodiment will be explained below in conjunction with a manufacturing process of the large EL display panel 10.

[0040] First, a plurality of EL panels 16 is prepared so that a display having a desired diagonal length will be obtained. In the present embodiment, four EL panels 16 are prepared.

[0041] In each of the EL panels 16, the required EL display device 14 is laminated on the sub transparent substrate 18 by the adhesive layer 20. The EL display device 14 is removed from the sub transparent substrate 18 at the adhesive layer 20 by applying the above-described separation and transfer technique.

[0042] The EL display devices 14 obtained from the EL panels 16 are arranged on the main transparent substrate 12 in a matrix pattern (2×2).

[0043] At this time, the circuit section 22C of the TFT layer 22 would overlap on the adjacent EL display device 14. To avoid this, the TFT layer 22 is bent at the boundary between the circuit section 22C and the pixel section 22P so as to dispose the circuit section 22C behind the adjacent EL display device 14.

[0044] The four EL display devices 14 are laminated on the main transparent substrate 12 by the adhesive layer 28 in a similar manner.

[0045] Accordingly, the circuit sections 22C of the TFT layers 22 are disposed behind the adjacent EL display devices 14, so that the gap between the pixels at the peripheries of the adjacent EL display devices 14 may be 10 μm . Thus, the four EL display devices 14A, 14B, 14C, and 14D appear to be unified, forming the large EL display panel 10.

(Second Embodiment)

[0046] A second embodiment of the present invention will be described below. The components which are similar to those in the first embodiment are denoted by the same reference numerals, and descriptions thereof are omitted.

[0047] In the second embodiment, the circuit section 22C of the TFT layer 22 is disposed in a manner such that the TFT layer 22 is not bent at the boundary between the circuit section 22C and the pixel section 22P.

[0048] As shown in Fig. 9, the main transparent substrate 12 is provided with a step portion 30 formed by the adhesive layer 28 which is used for laminating and fixing the EL display devices 14. The step portion 30 is provided so as to change the positions of adjacent EL display devices 14 in the thickness direction.

[0049] The height of the step portion 30 corresponds to the total thickness of the TFT layer 22 and the base layer 24. Accordingly, the circuit section 22C may be disposed behind the adjacent EL display device 14 without bending the TFT layer 22.

(Third Embodiment)

[0050] A third embodiment of the present invention will be described below. The components which are similar to those in the first embodiment are denoted by the same reference numerals, and descriptions thereof are omitted.

[0051] In the third embodiment, the circuit section 22C of the TFT layer 22 is also disposed in a manner such that the TFT layer 22 is not bent at the boundary between the circuit section 22C and the pixel section 22P.

[0052] In Fig. 10, in the third embodiment, the transparent electrode layer (not shown) is laminated on the main transparent substrate 12 as a first layer. In addition, the base layer 24 is provided as a second layer, and the TFT layer 22 is provided as a third layer. Accordingly, the transparent electrode layer 26, the base layer 24, and the TFT layer 22 are laminated inversely compared to the manner in the first and the second embodiment. In addition, the thickness of the pixel section 22P of the TFT layer 22 is double compared to that in the adjacent EL display device 14, which is the standard thickness.

[0053] Accordingly, the circuit section 22C is disposed behind the adjacent EL display device 14 without bending the TFT layer 22.

[0054] In the above-described second and third embodiments, the process of bending the TFT layer 22 at the boundary between the circuit section 22C and the pixel section 22P is not required. Thus, although the modifications such as changing of the thickness of the layers are required, the TFT layer 22 does not receive any load, so that problems such as contact failures are avoided.

(Fourth Embodiment)

[0055] The following embodiment may only be applied when four EL display devices 14 are provided. Fig. 11 shows a first modification in which the four EL display devices 14 are arranged in proximity to each other. In this case, four similar EL display devices 14 are prepared, and the two EL display devices 14 disposed in the right side and the two EL display devices 14 disposed in left side are arranged inversely to each other. Accordingly, the circuit sections 22C of the TFT layers 22 are prevented from overlapping on the adjacent EL display devices 14.

(Fifth Embodiment)

[0056] Fig. 12 shows a second modification in which the EL display devices 14 disposed at the right side and at the left side have different constructions. More specifically, the circuit sections 22C are disposed at symmetrical regions in the TFT layers 22. According to such a construction, the circuit sections 22C do not overlap on the adjacent EL display devices 14.

[0057] Although the large EL display panel 10 is constructed of the four EL display devices 14A, 14B, 14C, and 14D in the present embodiment, it is also possible to construct a larger panel.

[0058] Fig. 13 shows an actual manufacturing process of the EL panel 16 which is used for obtaining the large EL display panel 10.

[0059] The EL display devices 14 are formed by the processes shown in Fig. 13, which are described in order from the top. The order of the processes is: forming TFTS → forming an insulating interlayer → forming contact holes → forming a transparent electrode layer → forming banks → forming a hole transfer layer → forming an EL layer → forming an electrode layer.

Industrial Applicability

[0060] As described above, the following effects are obtained by the large EL and the manufacturing method therefor according to the present invention. That is, in the case in which a plurality of EL display devices are arranged in a matrix pattern, the pitch between the pixels provided in the pixel section of the TFT array is maintained constant.

Claims

1. A large EL panel comprising a plurality of EL display devices which are arranged in a matrix pattern on a main transparent substrate which is capable of supporting multiple EL display devices, each of said plurality of EL display devices including: a base layer over which a luminescent material is applied; an electrode layer which is laminated on one side of

said base layer; and a TFT layer including a circuit section in which light emission of the luminescent material is controlled by applying a predetermined voltage between said electrode layer and said TFT layer and a pixel section which is superimposed over the other side of said base layer and which is provided with a plurality of pixels which divide said base layer into sections so that light emission of the luminescent material in each section is individually controlled by generating a potential difference between said electrode layer and said TFT layer at the corresponding section,

wherein said circuit section of said TFT layer which is disposed at the region outside a light emitting area is disposed behind the adjacent EL display device so that light emitting areas of the adjacent EL display devices are arranged in proximity to each other with a predetermined amount of gap.

2. A large EL panel according to Claim 1, wherein said plurality of EL display devices are several inches in diagonal, and said main transparent substrate is 20 to 100 inches in diagonal.
3. A large EL panel according to one of Claim 1 and Claim 2, wherein said predetermined amount of gap between the light emitting areas of the adjacent EL display devices is approximately the same as a pitch between pixels provided in said pixel section of said TFT layer.
4. A large EL panel according to one of Claim 1 to Claim 3, wherein said TFT layer is bent at the boundary between said pixel section and said circuit section so as to dispose said circuit section of said TFT layer behind the adjacent EL display device.
5. A large EL panel according to one of Claim 1 to Claim 3, wherein a step portion for changing the positions of the adjacent EL display devices in the thickness direction is provided so as to dispose said circuit section of said TFT layer behind the adjacent EL display device.
6. A large EL panel according to Claim 5, wherein the adjacent EL display devices are inverted so as to arrange the base layers of the adjacent EL display devices in the same plane.
7. A manufacturing method for a large EL panel in which a plurality of EL display panels are used, each of said plurality of EL display panels being constructed of an EL display device and a sub transparent substrate for supporting said EL display device in parallel, said EL display device including: a base layer over which a luminescent material is applied; an electrode layer which is laminated on one

side of said base layer; and a TFT layer including a circuit section in which light emission of the luminescent material is controlled by applying a predetermined voltage between said electrode layer and said TFT layer and a pixel section which is superimposed over the other side of said base layer and which is provided with a plurality of pixels which divide said base layer into sections so that light emission of the luminescent material in each section is individually controlled by generating a potential difference between said electrode layer and said TFT layer at the corresponding section, said manufacturing method for the large EL display panel comprising the steps of: removing said EL display devices from said sub transparent substrates; arranging the obtained EL display devices on a main transparent substrate, which is broader than said sub transparent substrate, in a matrix pattern and in a manner such that light emitting areas of said adjacent EL display devices are in proximity to each other; and disposing and fixing said circuit section of said TFT layer, which is disposed at the region outside the light emitting area, behind the adjacent EL display device.

8. A manufacturing method for a large EL panel according to Claim 7, wherein said circuit section of said TFT layer is fixed in a manner such that said TFT layer is bent at the boundary between said circuit section and said pixel section and said circuit section is disposed behind the adjacent EL display device.
9. A manufacturing method for a large EL panel according to Claim 7, wherein the thickness of an adhesive layer used for laminating the EL display devices on said main transparent substrate is changed so as to form a step portion which changes the positions of the adjacent EL display devices in the thickness direction, so that said circuit section is disposed behind the adjacent EL display device.
10. A manufacturing method for a large EL panel according to Claim 7, wherein said adjacent EL display devices are inverted, and the thickness of said pixel portion of said TFT layer is increased so as to compensate for the displacement between the base layers of the adjacent EL display devices, which is caused as a result of the inversion, so that said circuit section is disposed behind the adjacent EL display device.

Fig. 1

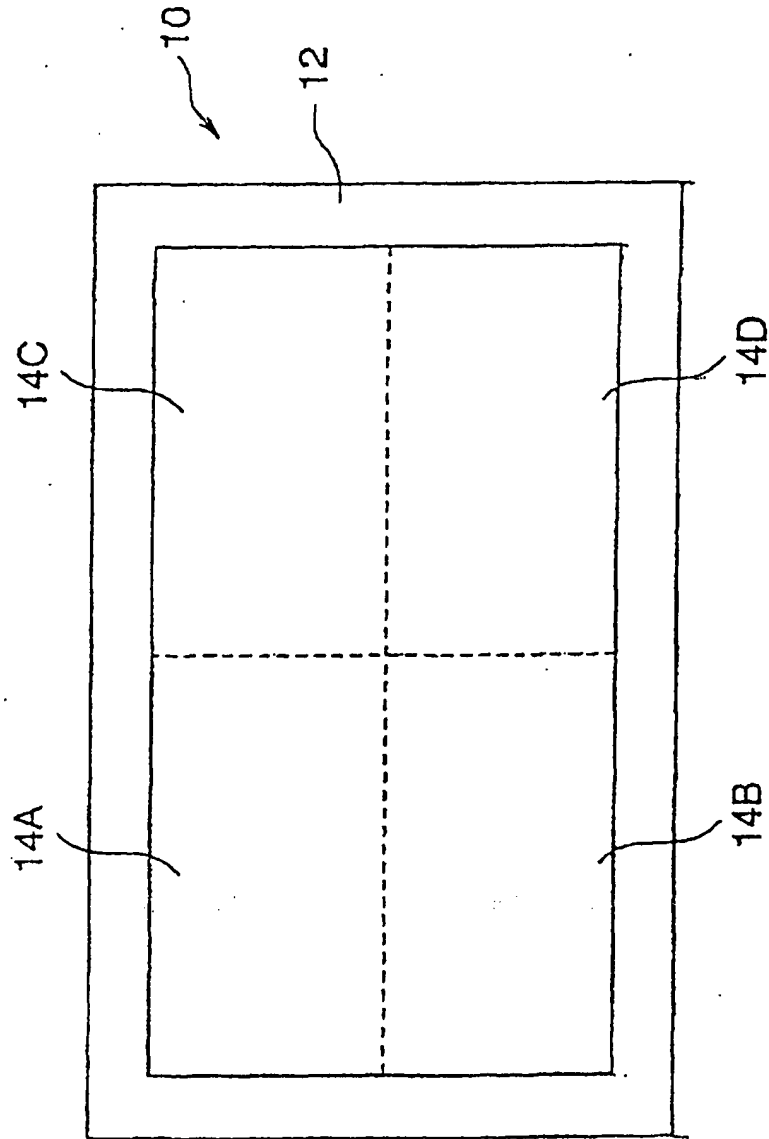


Fig. 2

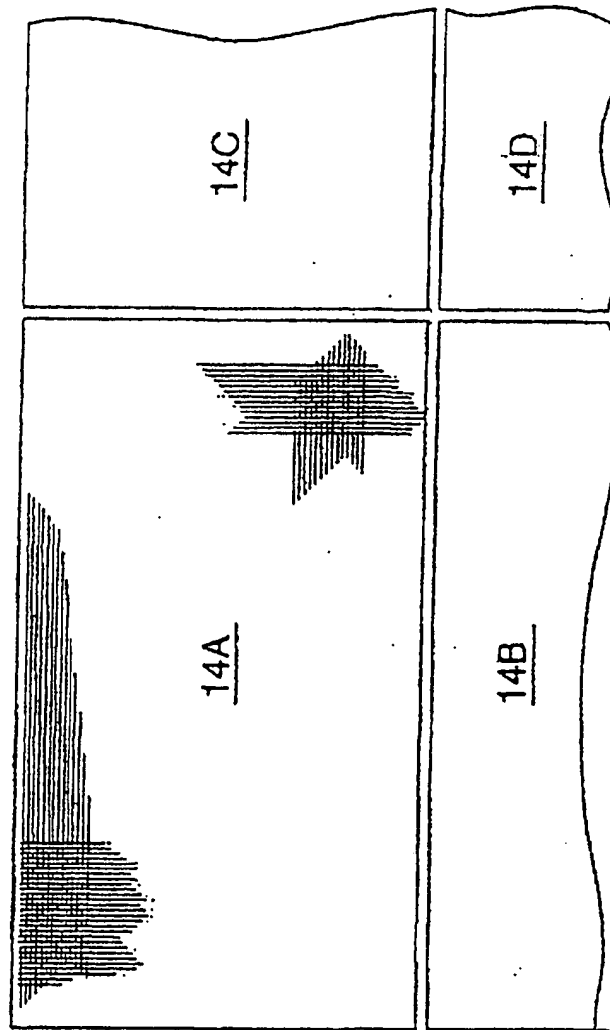


Fig. 3

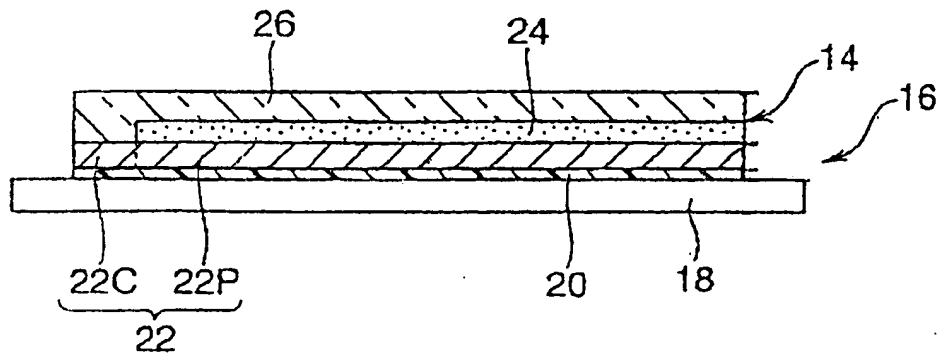


Fig. 4

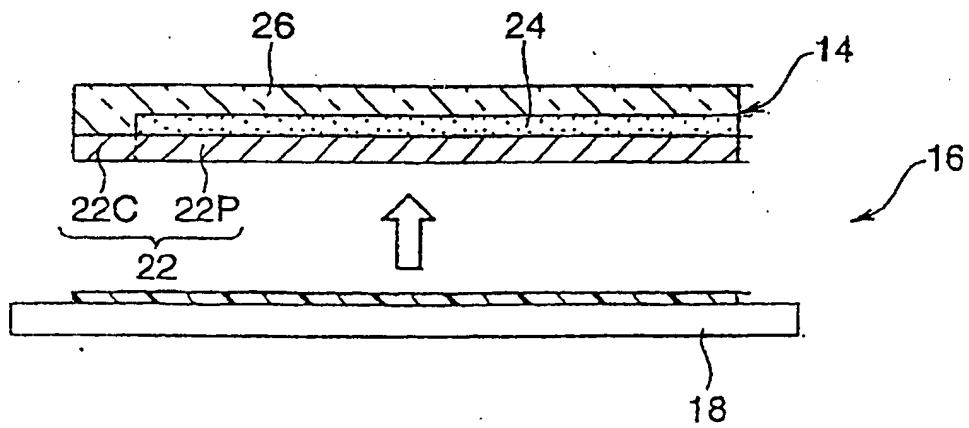


Fig. 5

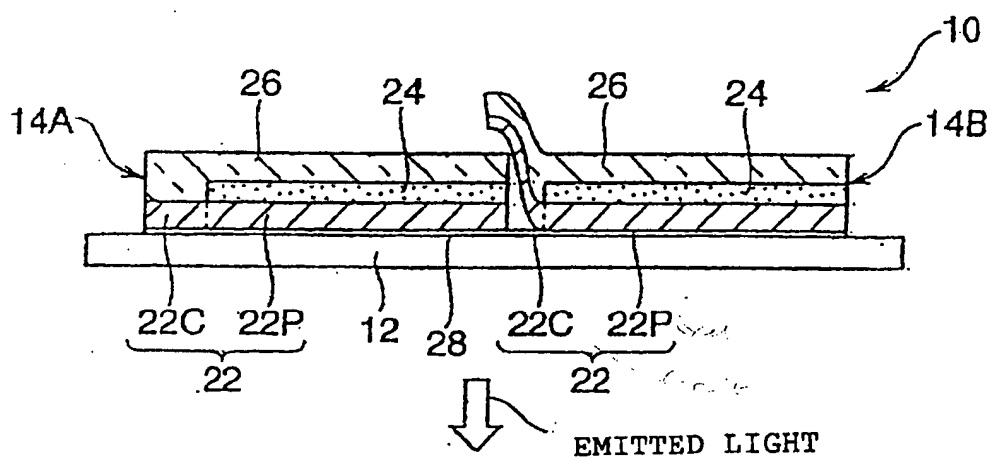


Fig. 6

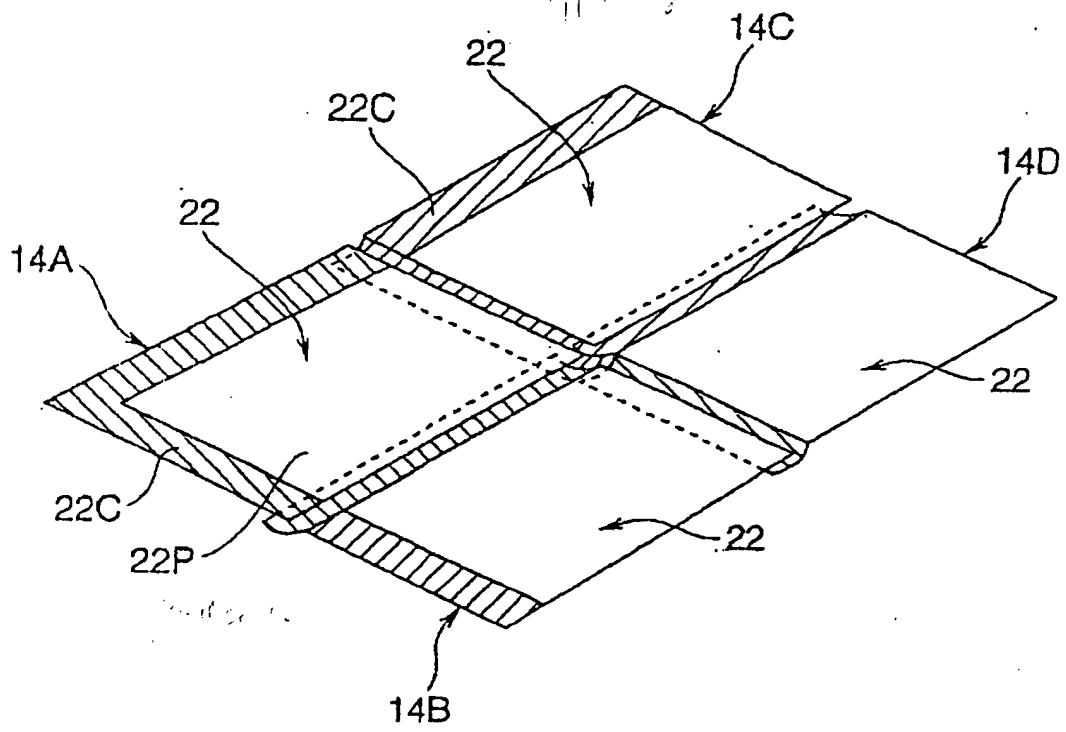


Fig. 7

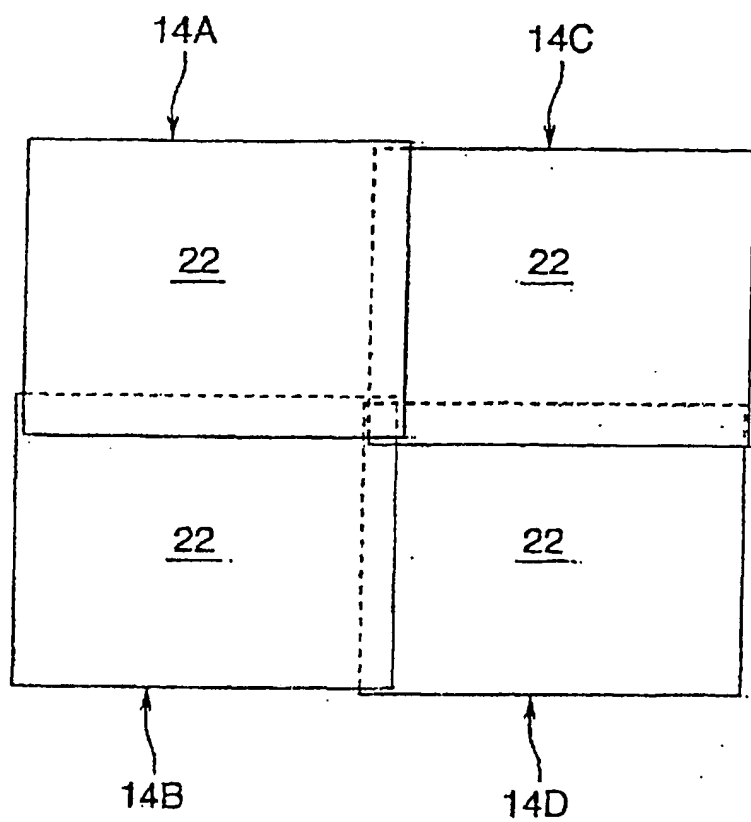


Fig. 8

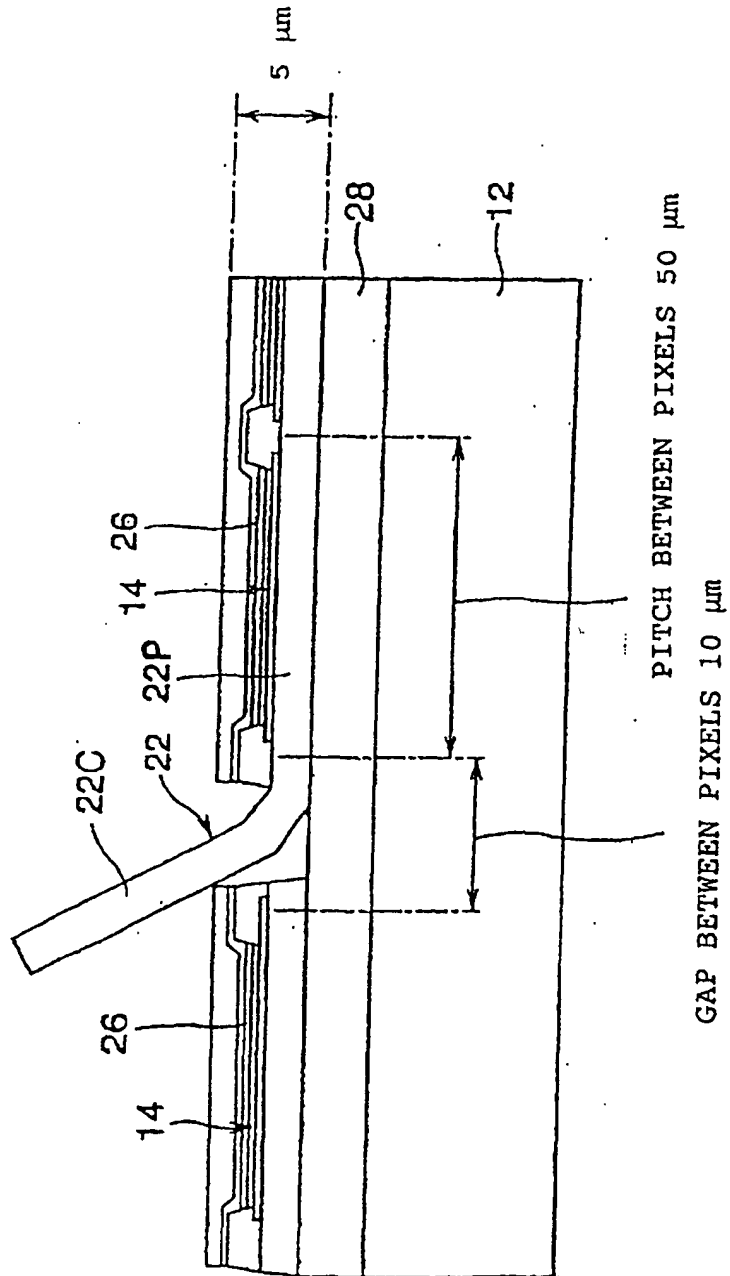


Fig. 9

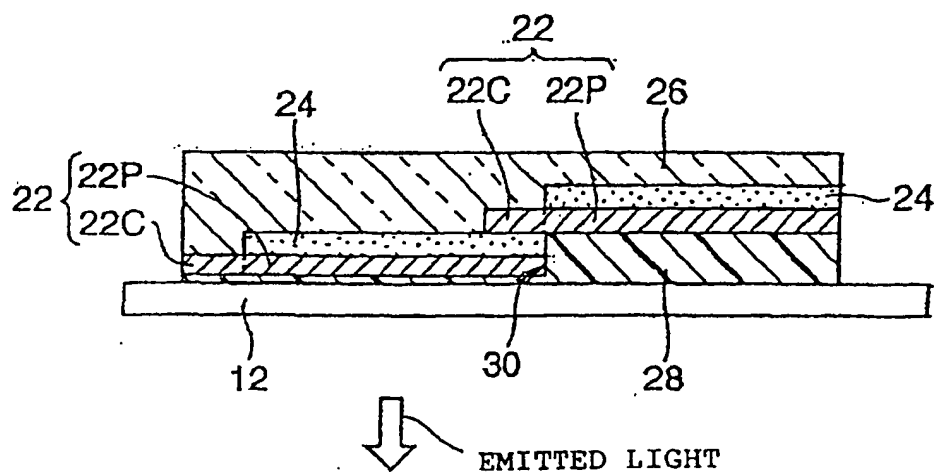


Fig. 10

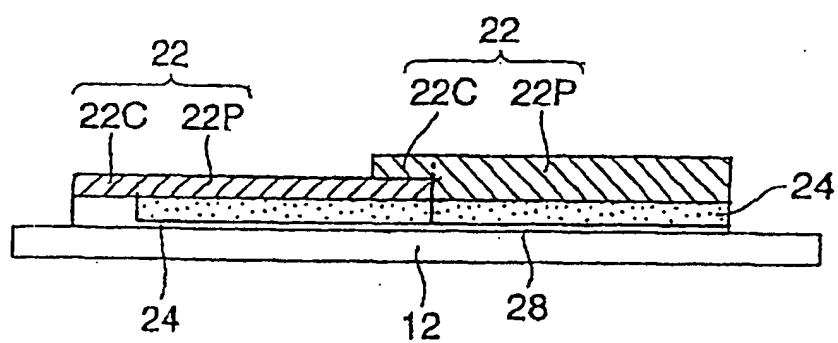


Fig. 11

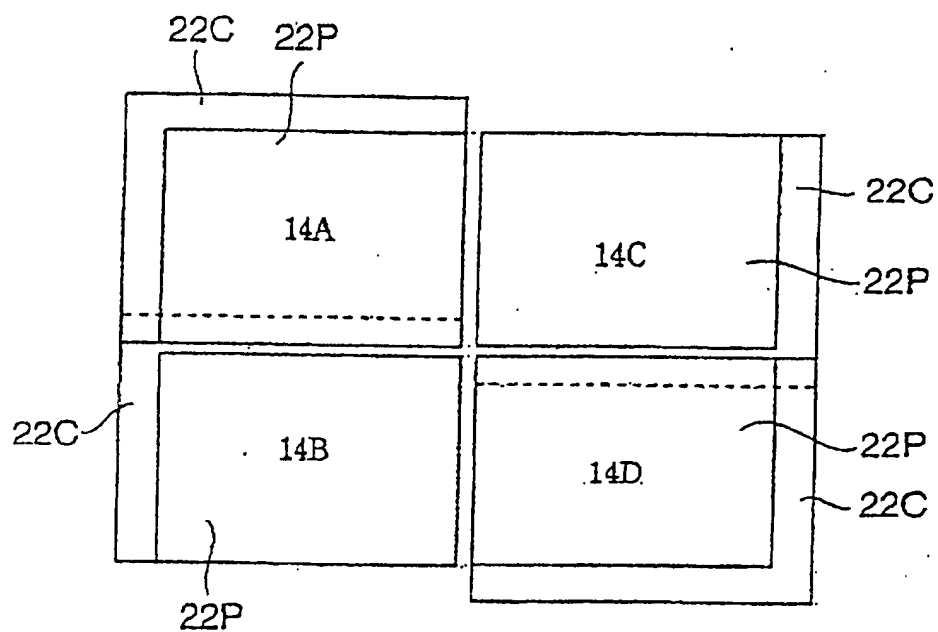


Fig. 12

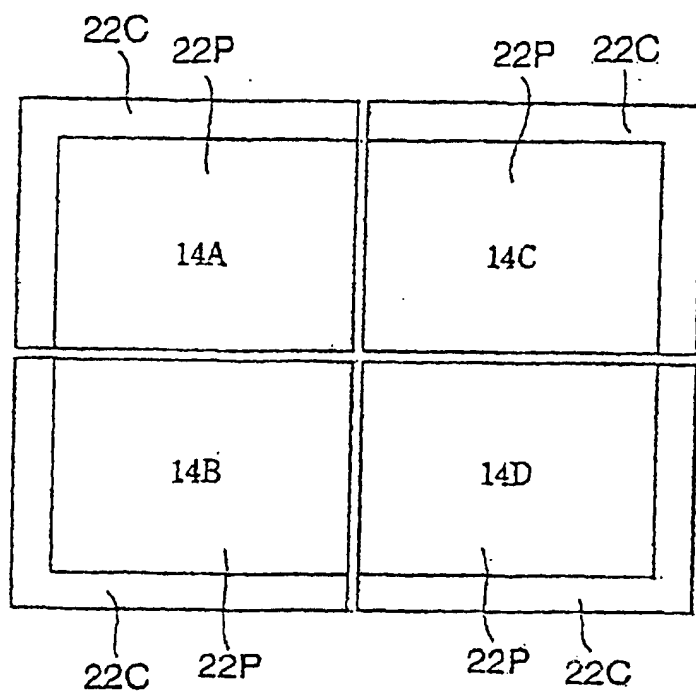
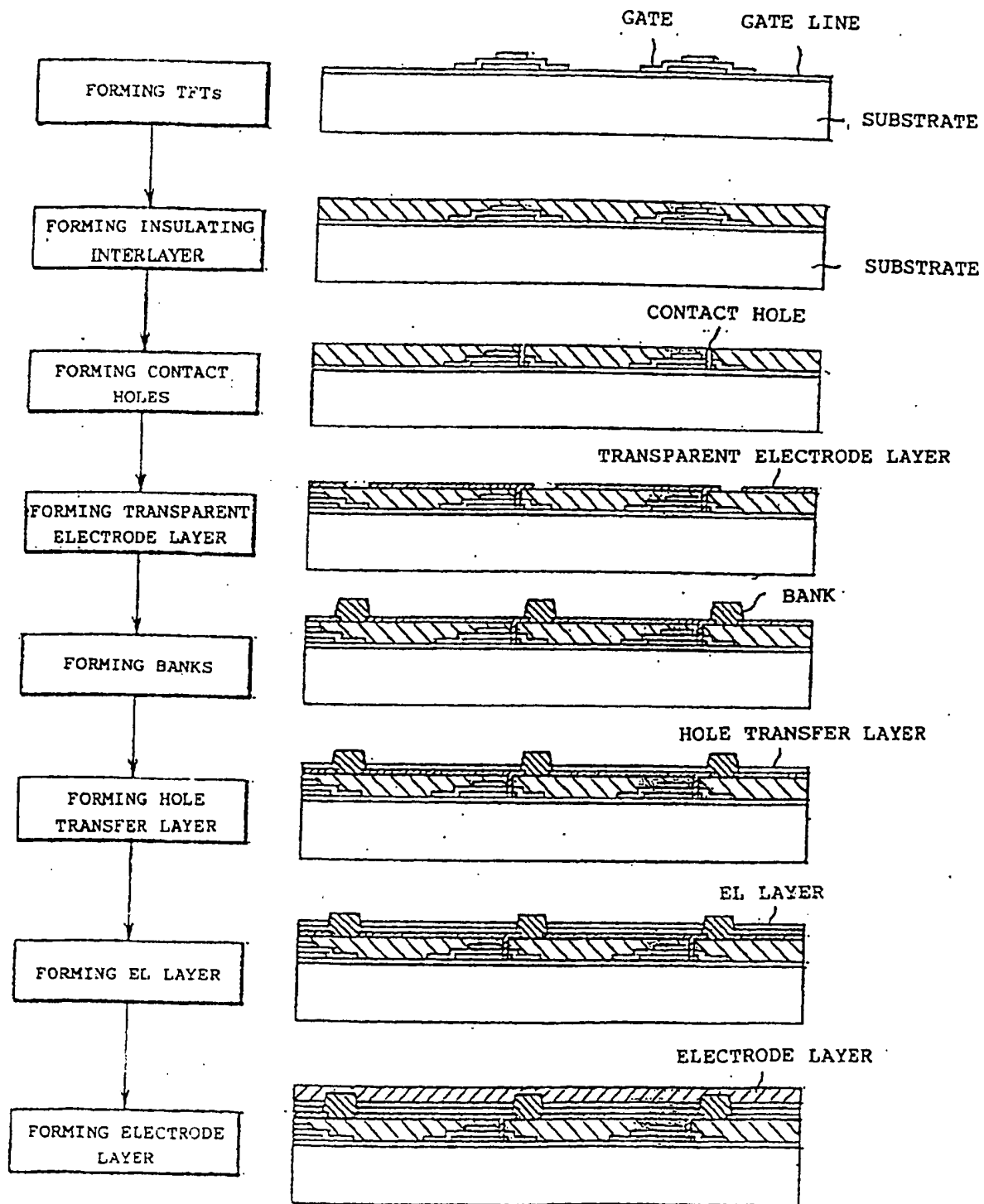


Fig. 13



INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP00/06622

A. CLASSIFICATION OF SUBJECT MATTER

Int.Cl.⁷ H05B 33/12, 33/10, 33/14
G09F 9/40, 9/30, 365

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

Int.Cl.⁷ H05B 33/00-33/28, G09F 9/30-9/40, G02F 1/133

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
 Jitsuyo Shinan Koho 1940-1996 Toroku Jitsuyo Shinan Koho 1994-2000
 Kokai Jitsuyo Shinan Koho 1971-2000 Jitsuyo Shinan Toroku Koho 1996-2000

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y A	JP, 6-61023, B2 (Stanley Electric Co., Ltd.), 10 August, 1994 (10.08.94), Full text (Family: none)	1-6 7-10
Y A	Microfilm of the specification and drawings annexed to the request of Japanese Utility Model Application No.42372/1983 (Laid-open No.147176/1984) (NEC Home Electronics Ltd.), 01 October, 1984 (01.10.84), Full text (Family: none)	1-6 7-10
Y A	JP, 1-297864, A (Matsushita Electric Ind. Co., Ltd.), 30 November, 1989 (30.11.89), Full text (Family: none)	1-6 7-10
Y A	US, 5550066, A (Eastman Kodak Company), 27 August, 1996 (27.08.96), Full text & JP, 8-241047, A	1-6 7-10
A	US, 5302468, A (Pioneer Electronic Corporation),	1-6

☒ Further documents are listed in the continuation of Box C. ☐ See patent family annex.

* Special categories of cited documents:	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
"A" document defining the general state of the art which is not considered to be of particular relevance	"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
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"O" document referring to an oral disclosure, use, exhibition or other means	
"P" document published prior to the international filing date but later than the priority date claimed	

Date of the actual completion of the international search
13 October, 2000 (13.10.00)

Date of mailing of the international search report
24 October, 2000 (24.10.00)

Name and mailing address of the ISA/
Japanese Patent Office

Authorized officer

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INTERNATIONAL SEARCH REPORT

International application No.

PCT/JP00/06622

C (Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
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